

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

**Bernhard Schaffer**

Serial No.: 10/765,620

Filing Date: **January 27, 2004**

Title: **Voltage Regulator with Frequency  
Response Correction**

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Group Art Unit: **2838**

Examiner:

Attny. Docket No. **068758.0160**

Client Ref.: **I0289US/lg**

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INFORMATION DISCLOSURE STATEMENT

Sir:

Applicants respectfully request, pursuant to 37 C.F.R. §§1.56, 1.97 and 1.98, that the art listed on the attached PTO-1449 form be considered and cited in the examination of the above-identified application. A copy of the cited art is enclosed for the convenience of the Examiner.

Furthermore, pursuant to 37 C.F.R. §§1.97(g) and (h), no representation is made that these references are material to the patentability of the present application.

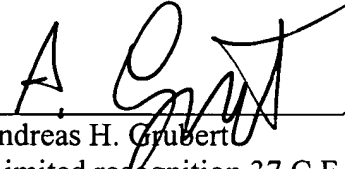
As the Information Disclosure Statement is being submitted before the mailing of the first office action on the merits, Applicants believe that no fee is required. If a fee is

required, please accept this transmittal as a petition therefor and charge any fee to Baker Botts L.L.P. (formerly, *Baker & Botts, L.L.P.*) Deposit Account No. 02-0383, Order No. (068758.0160) for any other charges necessary for the filing of this Information Disclosure Statement.

BAKER BOTTS L.L.P. (023640)

Date: June 21, 2004

By: \_\_\_\_\_

  
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PTO-1449  Information Disclosure Citation in an Application	Application No. <b>10/765,620</b>	Applicant(s): <b>Bernhard Schaffer</b>	
	Docket Number <b>068758.0160</b>	Group Art Unit <b>2838</b>	Filing Date <b>January 27, 2004</b>

**U.S. PATENT DOCUMENTS**

		DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
	1	4,908,566	03-13-90	Tesch	323	280	02-22-89
	2	5,191,278	03-02-93	Carpenter	323	275	10-23-91
	3	5,631,598	05-20-97	Miranda et al.	327	540	06-07-95
	4	5,852,359	12-22-98	Callahan, Jr. et al.	323	274	07-08-97
	5	5,889,393	03-30-99	Wrathall	323	282	09-29-97

**FOREIGN PATENT DOCUMENTS**

		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
	6	EP 0 766 164 A2	04-02-97	EPO	G05F	3/26	x	
	7	EP 0 846 996 A1	06-10-98	EPO	G05F	1/575	x	

**NON-PATENT DOCUMENTS**

		DOCUMENT (Including Author, Title, Source, and Pertinent Pages)	DATE
	8	G.A. Rincon-Mora et al.; "Optimized Frequency-Shaping Circuit Topologies for LDO's"; IEEE Transactions on Circuits and Systems II; Vol. 45; No. 6, pps. 703-708	1998
	9	R. Funke et al.; "Grundsichtungen der Elektronik" (Basic circuits of Electronics); Verlag Technik, Berlin, pages 24-34	1983
	10	H. Lindner et al.; "Elektrotechnik - Elektronik" (Electrical engineering - electronics); Fachbuchverlag Leipzig, pps. 591-606	1983

EXAMINER	DATE CONSIDERED
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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.